8596E-10

S	Е	R	V	Ι	С	Е	Ν	0	Т	
							SUPERSED	ES: None	;	
8590	6E Spe	ectrum	Anal	yzer						
Seria	ls:0000/	A00000 t	to 3513.	A99999						
	00001	U00000 t	o 99991	U99999						
Mod	lificatio	on to Δ	14   00	a Amol	ifier As	ssv to c	ontrol overs	hoot pr	oblem	
mou	mound			g Ampi		<i>boy to o</i>			obiein	
To B	e Perfor	med By:	Agilen	nt Person	nel or Q	ualified C	ustomer Person	nel		
<b>D</b> 1	• • •	• • •								
<b>Dupl</b> 8591]		rvice No	tes:							
8593										
8594										
8595]										
8596										
Parts	s Requir	red:								
Part	No.	Qty.		Descripti						
	-0124	1		Ferminal,						
	-4136	1				m 10 ufd.				
9320-	-6209	1	I	Label, 08	590-603	73				
									Continued	1

DATE: January 1996

## ADMINISTRATIVE INFORMATION

SERVICE NOTE CLASSIFICATION:										
MODIFICATION RECOMMENDED										
ACTION CATEGORY:	<ul> <li>IMMEDIATELY</li> <li>ON SPECIFIED FAILURE</li> <li>AGREEABLE TIME</li> </ul>	STANDARDS: Labor 1.5 Hour								
LOCATION CATEGORY:	CUSTOMER INSTALLABLE	SERVICE       □       RETURN       USED       □       RETURN         INVENTORY:       ■       SCRAP       PARTS:       ■       SCRAP         □       SEE TEXT       □       SEE TEXT								
AVAILABILITY:	PRODUCT'S SUPPORT LIFE	AGILENT RESPONSIBLE UNTIL: January 1998								
AUTHOR: PS	ENTITY: 5320	ADDITIONAL INFORMATION:								

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## Situation:

An overshoot problem has been observed when users make power vs time measurements on a PHS like signal even without the PHS DLP. The analyzer has this overshoot for fast turn on large dynamic range bursts (50 dB or so). This is generally associated with Digital Communication types of measurements.

Contact Paul Schmiedeberg at (707) 577-2941 or by desk memo and a packet of parts will be supplied for the modification.

## Solution/Action:

A 10 ufd. capacitor is added to the +11 volt regulated circuit. The capacitor is actually tied to the +11 V2 output to ground. The effect of the capacitor is to suppress the overshoot that has been experienced as described in the Situation description.

- 1. Lift the top lead of R112 from the circuit board at Point B, see Figure 1.
- 2. Place the terminal stud (p/n 0360-0124) into Point B and solder in place.
- 3. Place the (+) end of the capacitor (C78) into the hole at Point A and solder in place.
- 4. Wrap the (-) lead of C78 and the loose lead of R112 around the terminal stud in Point B and solder.
- 5. Apply one label with the 08590-60373 imprinted to the assembly over the 5062-8261 part number and place one label on the rear panel for quick identification of the modified Log assembly.

After re-assembly, the only calibration necessary is to run the Cal Freq and AmpEd self test from the front panel of the spectrum analyzer.

